

BAP65LX

Silicon PIN diode

Rev. 01 — 11 December 2007

Product data sheet

1. Product profile

1.1 General description

Planar PIN diode in a SOD882T leadless ultra small plastic SMD package.

1.2 Features

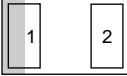

- High voltage, current controlled
- Low diode capacitance
- Low diode forward resistance (low loss)
- Very low series inductance
- RF resistor for RF switches

1.3 Applications

- RF attenuators and switches
- Band switch for TV tuners
- Series diode for mobile communication transmit-receive switch

2. Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Symbol
1	cathode	[1]	
2	anode		

Transparent top view

sym006

[1] The marking bar indicates the cathode.

3. Ordering information

Table 2. Ordering information

Type number	Package		
	Name	Description	Version
BAP65LX	-	leadless ultra small plastic package; 2 terminals; body 1 × 0.6 × 0.4 mm	SOD882T

4. Marking

Table 3. Marking

Type number	Marking code
BAP65LX	LF

5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_R	reverse voltage		-	30	V
I_F	forward current		-	100	mA
P_{tot}	total power dissipation	$T_{sp} = 90\text{ °C}$	-	135	mW
T_{stg}	storage temperature		-65	+150	°C
T_j	junction temperature		-65	+150	°C

6. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		78	K/W

7. Characteristics

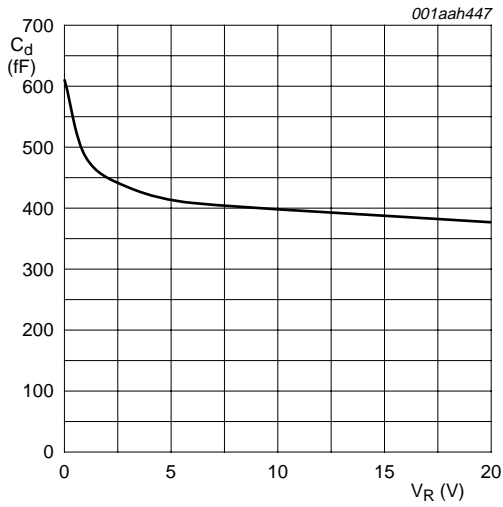
Table 6. Characteristics

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 50\text{ mA}$	-	0.9	1.1	V
I_R	reverse current	$V_R = 20\text{ V}$	-	-	20	nA
C_d	diode capacitance	see Figure 1 ; $f = 1\text{ MHz}$;				
		$V_R = 0\text{ V}$	-	0.61	-	pF
		$V_R = 1\text{ V}$	-	0.48	0.85	pF
		$V_R = 3\text{ V}$	-	0.43	0.7	pF
		$V_R = 20\text{ V}$	-	0.37	-	pF
r_D	diode forward resistance	see Figure 2 ; $f = 100\text{ MHz}$;				
		$I_F = 1\text{ mA}$	-	0.94	-	Ω
		$I_F = 5\text{ mA}$	-	0.58	0.95	Ω
		$I_F = 10\text{ mA}$	-	0.49	0.9	Ω
		$I_F = 100\text{ mA}$	-	0.35	-	Ω

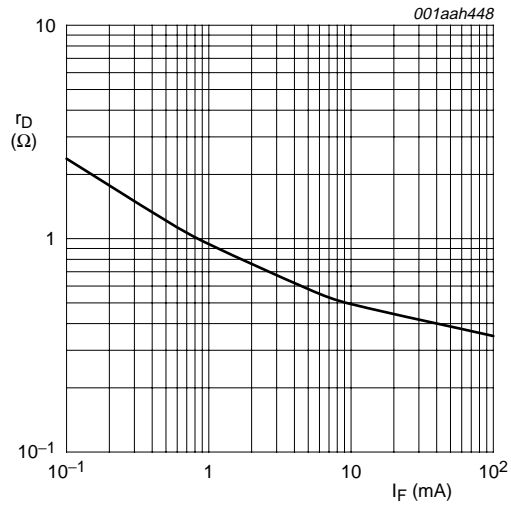
Table 6. Characteristics ...continued
 $T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
ISL	isolation	see Figure 3 ; $V_R = 0\text{ V}$;				
		$f = 900\text{ MHz}$	-	10	-	dB
		$f = 1800\text{ MHz}$	-	5.5	-	dB
		$f = 2450\text{ MHz}$	-	3.9	-	dB
L_{ins}	insertion loss	see Figure 4 ; $I_F = 1\text{ mA}$;				
		$f = 900\text{ MHz}$	-	0.09	-	dB
		$f = 1800\text{ MHz}$	-	0.09	-	dB
		$f = 2450\text{ MHz}$	-	0.10	-	dB
L_{ins}	insertion loss	see Figure 4 ; $I_F = 5\text{ mA}$;				
		$f = 900\text{ MHz}$	-	0.06	-	dB
		$f = 1800\text{ MHz}$	-	0.07	-	dB
		$f = 2450\text{ MHz}$	-	0.08	-	dB
L_{ins}	insertion loss	see Figure 4 ; $I_F = 10\text{ mA}$;				
		$f = 900\text{ MHz}$	-	0.06	-	dB
		$f = 1800\text{ MHz}$	-	0.07	-	dB
		$f = 2450\text{ MHz}$	-	0.08	-	dB
L_{ins}	insertion loss	see Figure 4 ; $I_F = 100\text{ mA}$;				
		$f = 900\text{ MHz}$	-	0.05	-	dB
		$f = 1800\text{ MHz}$	-	0.06	-	dB
		$f = 2450\text{ MHz}$	-	0.07	-	dB
τ_L	charge carrier life time	when switched from $I_F = 10\text{ mA}$ to $I_R = 6\text{ mA}$; $R_L = 100\text{ }\Omega$; measured at $I_R = 3\text{ mA}$	-	0.18	-	μs
L_S	series inductance	$I_F = 100\text{ mA}$; $f = 100\text{ MHz}$	-	0.4	-	nH



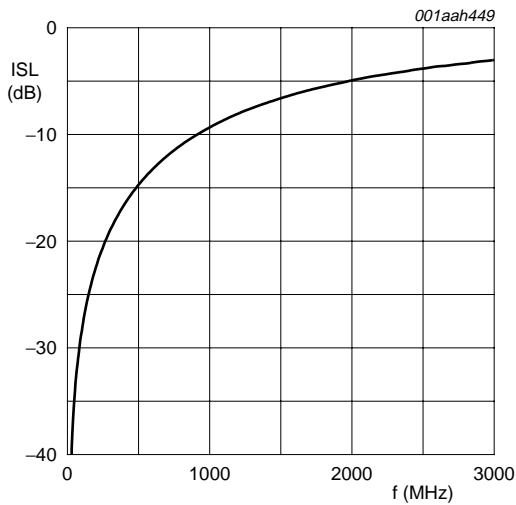
$f = 1 \text{ MHz}; T_j = 25 \text{ }^\circ\text{C}.$

Fig 1. Diode capacitance as a function of reverse voltage; typical values



$f = 100 \text{ MHz}; T_j = 25 \text{ }^\circ\text{C}.$

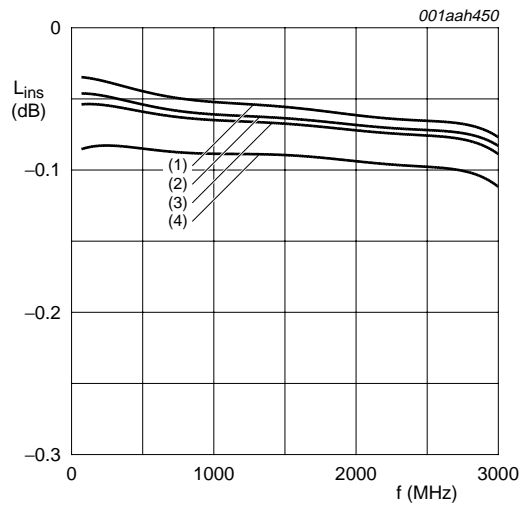
Fig 2. Forward resistance as a function of forward current; typical values



$T_{amb} = 25 \text{ }^\circ\text{C}$

Diode zero biased and inserted in series with a 50 Ω stripline circuit

Fig 3. Isolation of the diode as a function of frequency; typical values



$T_{amb} = 25 \text{ }^\circ\text{C}$

- (1) $I_F = 100 \text{ mA}$
- (2) $I_F = 10 \text{ mA}$
- (3) $I_F = 5 \text{ mA}$
- (4) $I_F = 1 \text{ mA}$

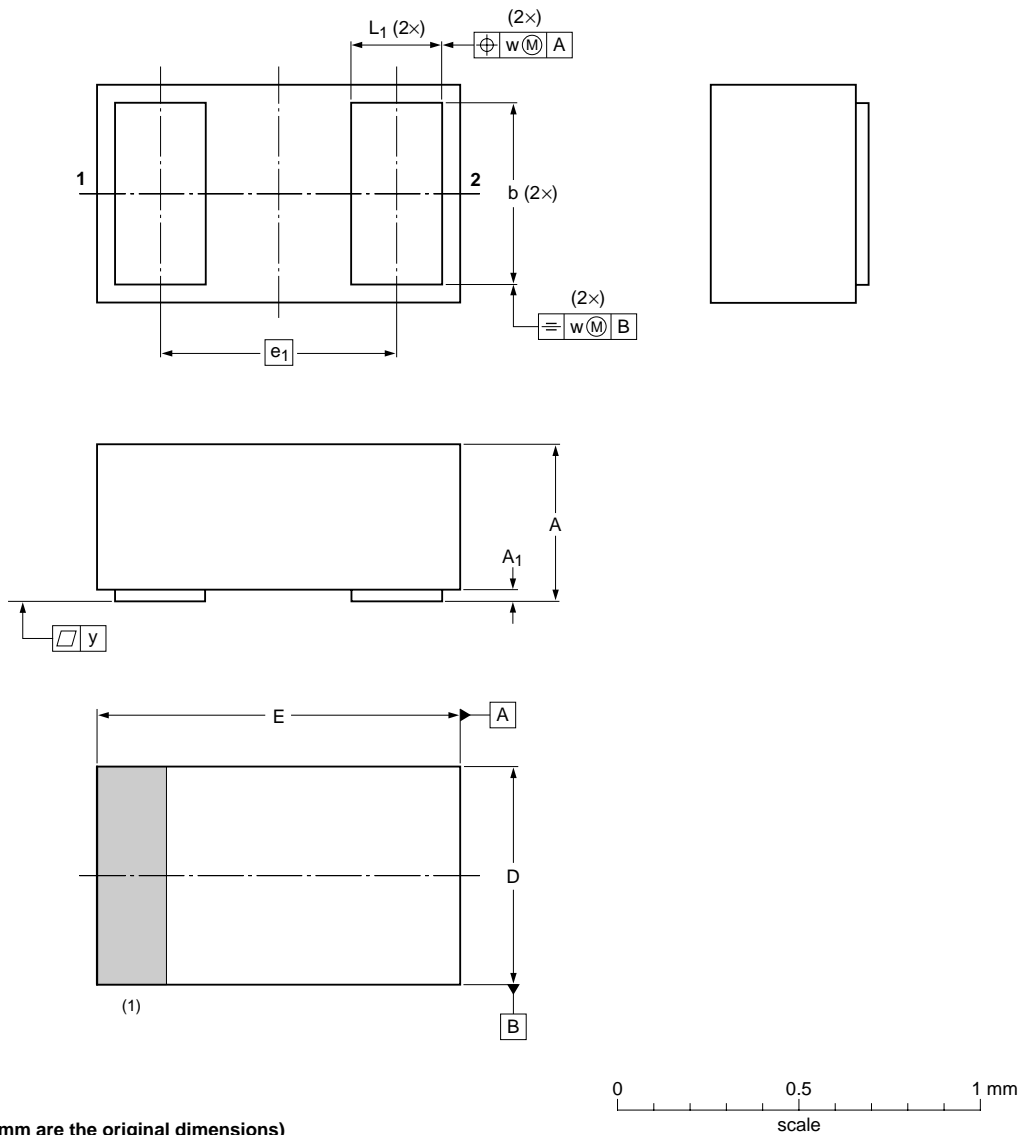
Diode inserted in series with a 50 Ω stripline circuit and biased via the analyzer Tee network

Fig 4. Insertion loss of the diode as a function of frequency; typical values

8. Package outline

Leadless ultra small plastic package; 2 terminals; body 1 x 0.6 x 0.4 mm

SOD882T



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b	D	E	e ₁	L ₁	w	y
mm	0.40 0.36	0.04	0.55 0.45	0.65 0.55	1.05 0.95	0.65	0.30 0.22	0.1	0.03

Note

1. The marking bar indicates the cathode

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOD882T					04-12-14 06-04-12

Fig 5. Package outline SOD882T

9. Abbreviations

Table 7. Abbreviations

Acronym	Description
PIN	P-type, Intrinsic, N-type
SMD	Surface Mounted Device
RF	Radio Frequency

10. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BAP65LX_1	20071211	Product data sheet	-	-

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

11.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

11.3 Disclaimers

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

11.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

12. Contact information

For additional information, please visit: <http://www.nxp.com>

For sales office addresses, send an email to: salesaddresses@nxp.com

13. Contents

1	Product profile	1
1.1	General description	1
1.2	Features	1
1.3	Applications	1
2	Pinning information	1
3	Ordering information	1
4	Marking	2
5	Limiting values	2
6	Thermal characteristics	2
7	Characteristics	2
8	Package outline	5
9	Abbreviations	6
10	Revision history	6
11	Legal information	7
11.1	Data sheet status	7
11.2	Definitions	7
11.3	Disclaimers	7
11.4	Trademarks	7
12	Contact information	7
13	Contents	8



Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2007.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 11 December 2007

Document identifier: BAP65LX_1